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FIG.1

MICROPROCESSOR 1

EXCEPTION PROCESSING UNIT — 8

DATA TLB — 9

INSTRUCTION EXECUTION UNIT — 4

PRIMARY CACHE — 5

20

CODE & DATA ENCRYPTION/ DECRYPTION UNIT — 6

ENCRYPTED ATTRIBUTE REGISTER

BUS INTERFACE UNIT — 7

3

SECONDARY CACHE — 10

BUS

INSTRUCTION TLB — 2

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FIG.2

	SIGNATURE		KEY VALUE	

KEY STORAGE DATA

30

20

SALT	OFFSET TO KEY STORAGE DATA	ALGORITHM	AREA END ADDRESS (AREA LENGTH)	KEY SIZE	AREA START ADDRESS

ENCRYPTED ATTRIBUTE REGISTER

FIG.3

KEY ENTRIES ENCRYPTED BY USING PUBLIC KEY OF PROCESSOR

11	Ekp[Ka]	
12	Ekp[Kb]	
13	Ekp[Kc]	
14	Ekp[Kd]	

ENCRYPTED ATTRIBUTE REGISTER   20

FOR Ta	s_addr1	e_addr1	Ka	22
FOR Tb	s_addr1	e_addr1	Kb	23
FOR Da	s_addr1	e_addr1	Ka	24
FOR Dc	s_addr1	e_addr1	Kc	25
FOR S	s_addr1	e_addr1	Kd	26

PROCESS SPACE   31

EXECUTION TEXT REGION	32
ENCRYPTED EXECUTION TEXT REGION Ta	35
ENCRYPTED EXECUTION TEXT REGION Tb	36
DATA REGION	33
ENCRYPTED DATA REGION Da	37
ENCRYPTED DATA REGION Dc	38
STACK REGION	34
(ENCRYPTED) STACK REGION S	39

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FIG.4

ADDRESS SPACE OF PROCESSOR A

41 — ENCRYPTED EXECUTION CODE/DATA

Ekp[Kx] 46

start_addr1
42
end_addr1

67 — Kha
67

DATA REGION Da

43
Ka 68

HIDDEN DATA REGION Ha

start_addr2
44
end_addr2

Xa
69 70
Kab

SHARED ENCRYPTED DATA REGION Sab

start_addr5
45
end_addr5

51 ENCRYPTED ATTRIBUTE REGISTER

start_addr1	end_addr1	Kx
start_addr2	end_addr2	Kha
start_addr5	end_addr5	Kab

52
53
54

KEY EXCHANGE

MAPPING 65

ADDRESS SPACE OF PROCESSOR B

Ekp[Ky] 66

start_addr3
end_addr3

start_addr4
end_addr4

start_addr6
end_addr6

61 — ENCRYPTED EXECUTION CODE/DATA

67 — Khb
67

DATA REGION Db

62

63
Kb 68

HIDDEN DATA REGION Hb

Xb
69 70
Kab

64

SHARED ENCRYPTED DATA REGION Sab'

71 ENCRYPTED ATTRIBUTE REGISTER

start_addr3	end_addr3	Ky
start_addr4	end_addr4	Khb
start_addr6	end_addr6	Kab

72
73
74

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FIG.5

| PROCESS A | S81 | PROCESS B | S91 |

S81 — SPECIFY Ekp[Kx] TO ENCRYPTED EXECUTION CODE/DATA REGION. PROCESSOR SETS START & END ADDRESSES & DECRYPTED KEY Kx IN ENCRYPTED ATTRIBUTE REGISTER.

S91 — SPECIFY Ekp[Ky] TO ENCRYPTED EXECUTION CODE/DATA REGION. PROCESSOR SETS START & END ADDRESSES & DECRYPTED KEY Ky IN ENCRYPTED ATTRIBUTE REGISTER.

S82 — SPECIFY KEY Kha FOR HIDDEN DATA REGION Ha. PROCESSOR SETS START & END ADDRESSES & KEY Kha IN ENCRYPTED ATTRIBUTE REGISTER WHILE HIDDEN DATA REGION Ha IS CREATED.

S92 — SPECIFY KEY Khb FOR HIDDEN DATA REGION Hb. PROCESSOR SETS START & END ADDRESSES & KEY Khb IN ENCRYPTED ATTRIBUTE REGISTER WHILE HIDDEN DATA REGION Hb IS CREATED.

S83 — CALCULATE KEYS Ka & Xa NECESSARY FOR KEY EXCHANGE PROCEDURE BY D-H SCHEME. KEY Xa IS STORED IN HIDDEN DATA REGION Ha. KEY Ka CAN BE STORED IN ORDINARY DATA REGION Da.

S93 — CALCULATE KEYS Kb & Xb NECESSARY FOR KEY EXCHANGE PROCEDURE BY D-H SCHEME. KEY Xb IS STORED IN HIDDEN DATA REGION Hb. KEY Kb CAN BE STORED IN ORDINARY DATA REGION Db.

S84 — SEND KEY Ka TO PROCESS B BY USING INTER-PROCESS COMMUNICATION FUNCTION (FOR PLAINTEXT) PROVIDED BY OS.

S94 — SEND KEY Kb TO PROCESS A BY USING INTER-PROCESS COMMUNICATION FUNCTION (FOR PLAINTEXT) PROVIDED BY OS.

S85 — RECEIVE KEY Kb FROM PROCESS B, & CALCULATE COMMON KEY Kab BY USING Kb & Xa.

S95 — RECEIVE KEY Ka FROM PROCESS A, & CALCULATE COMMON KEY Kab BY USING Ka & Xb.

S86 — CREATE SHARED ENCRYPTED DATA REGION Sab TO BE SHARED BETWEEN PROCESSES A & B, & REGISTER Sab AS SHARED MEMORY IN OS. PROCESSOR SETS START & END ADDRESSES & COMMON KEY Kab IN ENCRYPTED ATTRIBUTE REGISTER.

S96 — CREATE SHARED ENCRYPTED DATA REGION Sab' BY MAPPING Sab TO ADDRESS SPACE OF PROCESS B USING SHARING METHOD (attach) OF SHARED MEMORY PROVIDED BY OS. PROCESSOR SETS START & END ADDRESSES & COMMON KEY Kab IN ENCRYPTED ATTRIBUTE REGISTER.

S100 — OPERATIONS WITH RESPECT TO SHARED ENCRYPTED DATA REGION (READ, WRITE, ETC.)

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FIG.6

PROCESS A	SENT BY USING METHOD PROVIDED BY OS	PROCESS B
VERIFY COMPLETENESS OF Bcert		SEND RANDOM NUMBER Bn & CERTIFICATE Bcert
SEND RANDOM NUMBER An & CERTIFICATE Acert		VERIFY COMPLETENESS OF Acert
CALCULATE KEY EXCHANGE 1ST PHASE Av		CALCULATE KEY EXCHANGE 1ST PHASE Bv
SEND Av BY ATTACHING SIGNATURE		VERIFY COMPLETENESS OF Av
VERIFY COMPLETENESS OF Bv		SEND Bv BY ATTACHING SIGNATURE
CALCULATE COMMON KEY Kab		CALCULATE COMMON KEY Kab

VERIFICATION, Av/Bv CALCULATION, SIGNATURE ATTACHING & COMMON KEY CALCULATION ALGORITHM EXECUTION ARE CARRIED OUT IN ENCRYPTED INSTRUCTION EXECUTION MODE, & TEMPORARY DATA ARE CREATED & STORED IN ENCRYPTED DATA REGION.

FIG.7

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112 Ekp[Kx]

101 ADDRESS SPACE OF PROCESS A

ENCRYPTED EXECUTION CODE/DATA	107~ Kha	sa_1 102 ea_1
HIDDEN DATA REGION Ha 108~ Kab 109~ Kac	sa_2 103 ea_2
SHARED ENCRYPTED DATA REGION Sab (KEY EXCHANGE COMMUNICATION PATH) 110~ Kshare	sa_3 104 ea_3
SHARED ENCRYPTED DATA REGION Sac (KEY EXCHANGE COMMUNICATION PATH) 111~ Kshare	sa_4 105 ea_4
SHARED ENCRYPTED DATA REGION Hshare	sa_5 106 ea_5

113 Ekp[Ky]

121 ADDRESS SPACE OF PROCESS B

ENCRYPTED EXECUTION CODE/DATA 126~ Khb	sa_6 122 ea_6
HIDDEN DATA REGION Hb 127~ Kab	sa_7 123 ea_7
SHARED ENCRYPTED DATA REGION Sab' (KEY EXCHANGE COMMUNICATION PATH) 128~ Kshare	sa_8 124 ea_8
SHARED ENCRYPTED DATA REGION Hshare' 125	sa_9 ea_9

114 Ekp[Kz]

ADDRESS SPACE OF 131 132 PROCESS C

ENCRYPTED EXECUTION CODE/DATA 136~ Khc	sa_10 ea_10
HIDDEN DATA REGION Hc 137~ Kac	sa_11 133 ea_11
SHARED ENCRYPTED DATA REGION Sac' (KEY EXCHANGE COMMUNICATION PATH) 138~ Kshare	sa_12 134
SHARED ENCRYPTED DATA REGION Hshare''	sa_13 135 ea_13

MAPPING

NOTIFICATION OF KEY Kshare

MAPPING →

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FIG.8

STATE OF ENCRYPTED ATTRIBUTE REGISTER AT TIME OF EXECUTING PROCESS A

sa_1	ea_1	Kx
sa_2	ea_2	Kha
sa_3	ea_3	Kab
sa_4	ea_4	Kac
sa_5	ea_5	Kshare

STATE OF ENCRYPTED ATTRIBUTE REGISTER AT TIME OF EXECUTING PROCESS B

sa_6	ea_6	Ky
sa_7	ea_7	Khb
sa_8	ea_8	Kab
sa_9	ea_9	Kshare

STATE OF ENCRYPTED ATTRIBUTE REGISTER AT TIME OF EXECUTING PROCESS C

sa_10	ea_10	Kz
sa_11	ea_11	Khc
sa_12	ea_12	Kac
sa_13	ea_13	Kshare

211, 212, 213, 214, 215, 216, 221, 222, 223, 224, 225, 231, 232, 233, 234, 235